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AMENDMENTS TO THE CLAIMS

Please amend claim 18 and 23 as follows:

- 1-17. (Cancelled).
- 18. (Currently Amended) A voltage regulator comprising:

a series type regulator which is supplied with a <u>first</u> reference voltage and a first voltage, and which is coupled to an output <u>node</u>, node; and

wherein the series type regulator comprises:

a first amplification circuit which amplifies a first voltage difference between the first reference voltage and the first voltage; and

a first transistor which is coupled between a first node and the output node and which is supplied with the amplified first voltage difference; and

a shunt type regulator which is supplied with the <u>a second</u> reference voltage and a second voltage, and which is coupled to the output node,

wherein the shunt type regulator comprises:

a constant current source which is coupled between a power supply voltage the first node and the output node and which supplies a constant current to the output node;

<u>a second</u> an amplification circuit which amplifies a <u>second</u> voltage difference between the second voltage and the <u>second</u> reference voltage; and

a <u>second</u> transistor which is coupled between the output node and a ground voltage <u>second node</u> and which is controlled by an output voltage of the amplification circuit <u>supplied with the amplified second voltage difference</u>.

- 19. (Previously Presented) The voltage regulator according to claim 18, wherein voltage levels of the first and second voltages are the same as each other.
- 20. (Previously Presented) The voltage regulator according to claim 18, wherein voltage levels of the first and second voltages differ from each other.

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21. (Previously Presented) The voltage regulator according to claim 20, wherein a voltage level of the second voltage is lower than a voltage level of the first voltage.

- 22. (Previously Presented) The voltage regulator according to claim 18, wherein each of the first and second voltages is generated by dividing a voltage level of the output node.
- 23. (Currently Amended) The voltage regulator according to claim 20, wherein the first node is supplied with a power supply voltage and the second node is supplied with a ground voltage.

the series type regulator comprises a first input node which is supplied with the first voltage and a second input node which is supplied with the reference voltage, wherein the shunt type regulator further comprises a third input node which is supplied with the second voltage and a fourth input node which is supplied with the reference voltage, and wherein the voltage regulator further comprises:

a first node which is supplied with the first voltage and which is coupled between the output node and the first input node;

a second node which is supplied with the second voltage and which is coupled between the ground voltage and the third input node; and

a resistance element which is coupled between the first and second nodes.